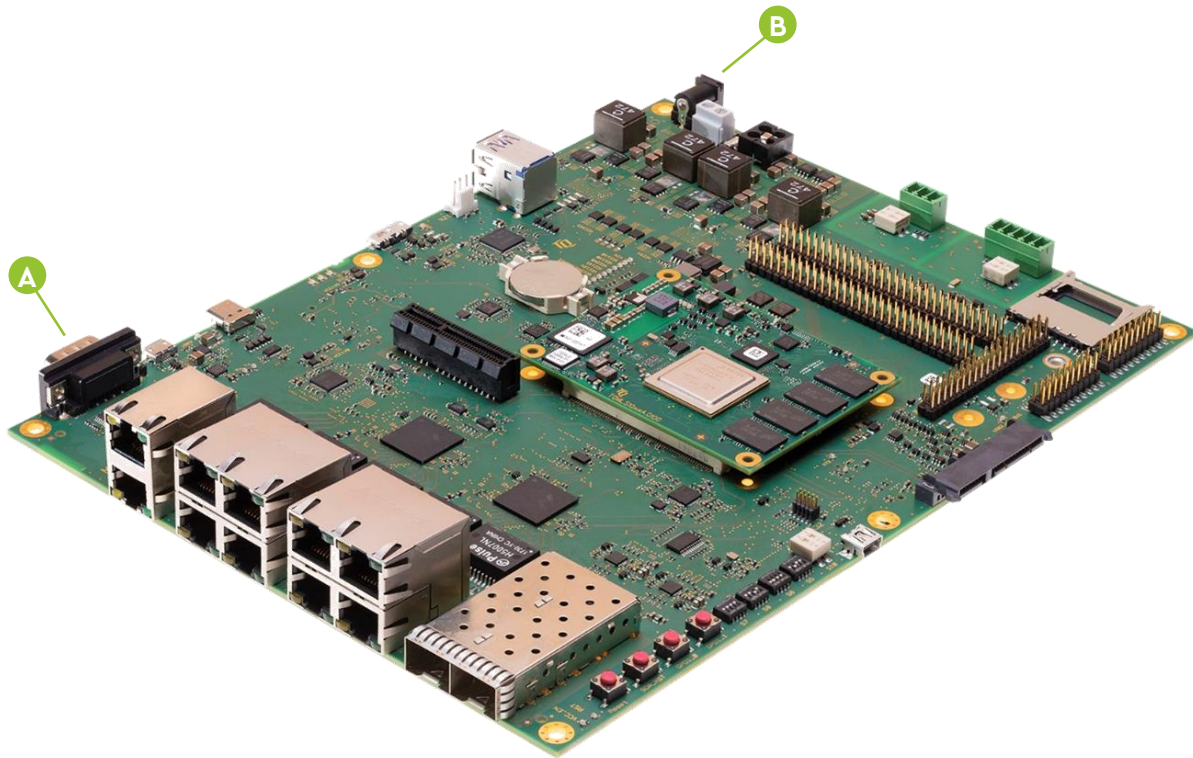


Quick-Start-Guide STKLS10xxA

QorIQ® Layerscape



A CONNECT HOST PC AND STKLS10xxA

Use the supplied null modem cable to connect COM1 of the STKLS10xxA (X29) to a serial port on your host PC (e.g. COM1/ttyS0).

RUN TERMINAL EMULATOR AND CONFIGURE SERIAL PORT

Run your favourite terminal emulator on the host PC (we recommend Tera Term Pro) and configure the serial port as follows:

Baudrate	115200
Data bits	8
Parity	none
Stop bits	1
Handshake	XON / XOFF

B SUPPLY THE STKLS10xxA WITH POWER

Double-check the mains voltage required for the included Power supply, then connect it to connector X14 on the STKLS10xxA.

Caution when using a different power supply! The Starterkit can be operated using a regulated supply voltage of 18-28V (nom. 24V)!

ESTABLISH SERIAL CONNECTION

The boot messages of boot loader and operating system are displayed by the terminal emulator.

! For latest documentation, BSPs, etc. please visit:

[tq-group.com/en/products/tq-embedded/qorik-layerscape/tqmls1043a](https://www.tq-group.com/en/products/tq-embedded/qorik-layerscape/tqmls1043a)
[tq-group.com/en/products/tq-embedded/qorik-layerscape/tqmls1046a](https://www.tq-group.com/en/products/tq-embedded/qorik-layerscape/tqmls1046a)
[tq-group.com/en/products/tq-embedded/qorik-layerscape/tqmls1088a](https://www.tq-group.com/en/products/tq-embedded/qorik-layerscape/tqmls1088a)

[Support.tq-group.com/tqmls1043a](https://support.tq-group.com/tqmls1043a)
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Default DIP switch setting

Boot device selection

To select the desired boot device set DIP switch **S5** accordingly.

Configuration SD Card

S5				
DIP	1	2	3	4
Set	OFF	OFF	ON	X

Configuration eMMC

S5				
DIP	1	2	3	4
Set	ON	OFF	ON	X

Configuration QSPI

S5				
DIP	1	2	3	4
Set	OFF	ON	ON	X

MBLS10xxA Configuration DIP (S6,S7,S8,S10)

S6		
DIP	ON	OFF
1	SerDes1 Lane A - SGMII	SerDes1 Lane A - QSGMII
2	SerDes1 Lane B - SGMII	SerDes1 Lane B - QSGMII
3	SerDes1 Lane D - SGMII	SerDes1 Lane D - XFI2
4	SerDes2 Lane B - MPCle	SerDes2 Lane B - SGMII

S7		
DIP	ON	OFF
1	QSGMII1 CLK SEL - 156.25 MHz	QSGMII1 CLK SEL - 156.25 MHz
2	QSGMII PHY1 CONFIG - QSGMII to Copper	QSGMII PHY1 CONFIG - SGMII to Copper
3	SerDes1 Lane D - SGMII	SerDes1 Lane D - XFI2
4	SerDes2 Lane B - mPCle	SerDes2 Lane B - SGMII